							Sheet 1 of	- 4			
U.S. Depar	tment	of Commerce,	Patent and	Trademark Office	Applica	tion No.: U	Inknown 10/7	22			
			Filing date: Nov. 25, 2003								
INFO	RMATI	ON DISCLOSURE S	Inventors: Nagakura, et al.								
						Group Art Unit: Unknown					
"Segment Buffer Loading In A Deinterlacer" Express Mail No.: ER452602418US						Examiner name: Unknown Attorney Docket No. VIM-004					
Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date, If Appropriate				
SH ·	AA	6,339,434	1/15/02	West et al.	345						
SH	AB	6,252,906	6/26/01	Canfield	375	240.21					
	AC										
	AD										
	ΑE					_					
			Foreig	gn Patent Documents	5						
_						Translation					
	AF	Document - · · · Number	Date -	· - · · Country	_Class _	Subclass	_ Yes_	No			
		THER ART (Inclu	ding Autho	or Title Date Pe	ertinent 1	Pages Etc	\				
SH AG		OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.) Internet pages from Pixelworks Inc. website www.pixelworksinc.com , first page entitled "PixelWorks Products" 3 pages, printed out 6/27/02.									
	AH	Internet pages from of Genesis Microchip Inc. www.genesis-microchip.com , first page entitled "Genesis Any Source – Any Display – gm6010 Digital TV Controller, 4 pages, downloaded 6/27/02.									
	AI	Trident DPTV-iX Preliminary Product Brief from Trident Microsystems, Inc., 12 pages, dated November 2001.									
	ΑJ	Genesis FLI2300 Digital Video Format Converter Product Brief from Genesis Microchip Inc., 2 pages, printed out 6/27/02.									
	AK	Genesis gm6010 and gm6015 Product Brief from Genesis Microchip Inc., 2 pages, printed out 6/27/02.									
	AL	U.S. Patent Application Serial No. 10/150,685, atty docket no. 013856-000006, entitled "Distrubuted RAM In A Logic Array", 21 pages (and copy of filing receipt), filed 5/17/02.									
\bigvee	AM	U.S. Patent Application Serial No. 10/164,455, atty docket no. 013856-000001, entitled "Cell Architecture To Reduce Customization In A Semiconductor Device", 33 pages (and copy of filing receipt), filed 6/6/02.									
SH	AO		_	rocessor product description				ted			
xaminer /	/Sher	rie Hsia/	Date Cons	sidered	06/21/2006						
*EXAMINER: MPEP 609; I	Init Draw	ial if reference line through ci	ce consider	red, whether or not not in conformance on to applicant.	citatio	n is in con	formance wi . Include	th			

Sheet 2 of 2

U.S. Department of Commerce, Patent and Trademark Office						Application No.: Unknown 10/1132						
						Filing date: Nov. 25, 2003						
INFORMATION DISCLOSURE STATEMENT BY APPLICANT						Inventors: Nagakura, et al.						
						Group Art Unit: Unknown						
"Segment Buffer Loading In A Deinterlacer"						Examiner name: Unknown						
Express Mail No.: ER452602418US						Attorney Docket No. VIM-004						
						<u> </u>						
*Examiner Document Piling Date,												
Initial		Number	Date Name		Class		Subclass		If Appropr			
SH	ВА	5,027,203										
	BB	5,162,907										
	ВС	5,410,356										
	BD	5,428,397						:				
	BE	5,473,383	-							1		
	BF	5,521,644					1		-			
	BG	5,682,205					1				-	
	BH-	5,689,305					-					
	BI	6,133,957										
	BJ	6,166,773								1		
V	BK	6,348,949										
SH	BL	6,577,345					-					
	вм							_				
	BN					-						
				Forei	n Patent	Documents						
	Document						Transl					ion
	BO Document Number		Date Country		ntry	Class		Subclass		Yes	No	
	o	THER ART (Inclu	ding	Autho	or, Title,	Date, Pe	rtine	nt E	ages,	Etc.)	
	BP											
	BQ											
Examiner /Sherrie Hsia/		Date Considered				06/21/2006						
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant												